

Docket No.: WMP-IFT-679

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Date: October 15, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/662,793
Applicant : Alfred Hesener
Filed : September 15, 2003
Art Unit : to be assigned
Examiner : to be assigned

Docket No. : WMP-IFT-679
Customer No. : 24131

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner for Patents

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 4,541,041 (Park et al.), dated September 10, 1985;

United States Patent No. 4,855,888 (Henze et al.), dated August 8, 1989;


German Published Non-Prosecuted Patent Application DE 32 41 738 A1
(Raducanu), dated May 17, 1984, and English abstract thereof;

Jens Peer Stengl et al.: "Leistungs-MOS-FET-Praxis" [power MOSFET practice],
Pflaum Verlag, München, 2nd ed., 1992, pp. 173-176;

U. Tietze et al.: "Halbleiter-Schaltungstechnik" [semiconductor circuit technology],
*Springer Verlag, Berlin, 9th ed., pp. 561-576, and pp. 502-515 of the corresponding
English translation.*

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,


For Applicant

Mark P. Weichselbaum
Reg. No. 43,248

Date: October 15, 2003

Lerner And Greenberg, P.A.
Post Office Box 2480
Hollywood, FL 33022-2480
Tel: (954) 925-1100
Fax: (954) 925-1101

/bmb

FORM PTO-1449 (SUBSTITUTE)

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

Attorney Docket No.:

Applic. No.

WMP-IFT-679

10/662,793

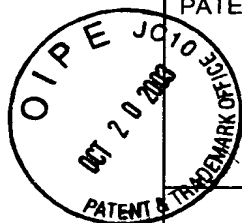
Applicant

Alfred Hesener

Filing Date

Group Art Unit

September 15, 2003

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT
(37 CFR 1.98(b))

U.S. PATENT DOCUMENTS

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	4,541,041	09/10/85	Park et al.			
	B	4,855,888	08/08/89	Henze et al.			
	C						
	D						
	E						
	F						
	G						
	H						
	I						

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
	J	32 41 738 A1	05/17/84	Germany			X
	K						
	L						
	M						
	N						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	O	Jens Peer Stengl et al.: "Leistungs-MOS-FET-Praxis" [power MOSFET practice], <i>Pflaum Verlag, München, 2nd ed., 1992, pp. 173-176</i>
	P	U. Tietze et al.: "Halbleiter-Schaltungstechnik" [semiconductor circuit technology], <i>Springer Verlag, Berlin, 9th ed., pp. 561-576, and pp. 502-515 of the corresponding English translation</i>

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.